

**IN THE SPECIFICATION**

1. Please amend the "Cross Reference to Related Applications" section beginning on page 1, line 5 of the application as follows:

The present invention is related to those disclosed in the following United States Non-Provisional Patent Applications:

1) ~~{Docket No. NATH15-04951}~~, Serial No. 09/993,283 filed on November 6, 2001 ~~concurrently herewith~~, entitled "CHARGE PUMP CIRCUIT FOR A HIGH SPEED PHASE LOCKED LOOP", now U.S. Patent No. 6,611,161; and

2) ~~{Docket No. NATH15-04953}~~, Serial No. 09/992,950 filed on November 6, 2001 ~~concurrently herewith~~, entitled "LOCK DETECTION CIRCUIT FOR A PHASE LOCKED LOOP CIRCUIT".

The above patent applications are commonly assigned to the assignee of the present invention. The disclosures of these related patent applications are hereby incorporated by reference for all purposes as if fully set forth herein.

2. Please amend the paragraph beginning on page 6, line 23 of the application as follows:

According to a further embodiment of the present invention, the loop response control circuit, for a given value of N, sets  $I_c$  to a maximum current level when M is in the range  $1 \leq M \leq J$  and sets  $I_c$  to a second current level lower than the maximum current level when M is in the range  $K+1 \leq \underline{N M} \leq Q$ .

3. Please amend the paragraph beginning on page 7, line 7 of the application as follows:

According to a yet further embodiment of the present invention, the loop response control circuit sets  $I_c$  to a third current level lower than the second current level when M is in the range  $Q+1 \leq \underline{N M} \leq T$ .